# Pattern Formation on Silicon-on-Insulator

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## ABSTRACT

The strain driven self-assembly of faceted Ge nanocrystals during epitaxy on Si(001) to form quantum dots (QDs) is by now well known. We have also recently provided an understanding of the thermodynamic driving force for directed assembly of QDs on bulk Si (extendable to other QD systems) based on local chemical potential and curvature of the surface. Silicon-on-insulator (SOI) produces unique new phenomena. The essential thermodynamic instability of the very thin crystalline layer (called the template layer) resting on an oxide can cause this layer, under appropriate conditions, to dewet, agglomerate, and self-organize into an array of Si nanocrystals. Using low-energy electron microscopy (LEEM), we observe this process and, with the help of first-principles total-energy calculations, we provide a quantitative understanding of this pattern formation. The Si nanocrystal pattern formation can be controlled by lithographic patterning of the SOI prior to the dewetting process. The resulting patterns of electrically isolated Si nanocrystals can in turn be used as a template for growth of nanostructures, such as carbon nanotubes (CNTs). Finally we show that this growth may be controlled by the flow dynamics of the feed gas across the substrate.

# **INTRODUCTION**

Silicon-on-insulator substrates are formed by a SiO<sub>2</sub> layer sandwiched between a thin crystalline top Si layer (the template layer) and a thick Si (handle) wafer. This arrangement makes it very attractive for use as a substrate in the fabrication of low-power, low-voltage devices and advanced devices requiring thin geometries. As devices shrink in lateral size, the template layer must shrink correspondingly in order to maintain the benefits of an SOI structure. This ultra-thin configuration, however, is only quasi-stable, thermodynamically. As the template film is made thinner, the surface free energy begins to overcome the kinetic limitations on its geometry [1-4]. Upon heating, such films may act to minimize their surface free energy by changing their morphology, typically via dewetting the unstable interface and agglomeration into 3D nanocrystals (see review [5]). Such behavior has obvious implications for semiconductor device processing, but can potentially be exploited to generate useful nanostructures as well.

Earlier work on SOI decomposition has noted that template layers on the order of tens of nanometers in thickness will agglomerate in patterns that are aligned in the <310> directions, but has not been able to explain this behavior. This study proposes a mechanism describing the

source of ordering of nanocrystals into networks by thermal dewetting of SOI and presents a novel application for such structures as templates for ordered growth of carbon nanotubes.

# **EXPERIMENTAL DETAILS**

Silicon-on-insulator wafers are commercially available with template layer thicknesses on the order of hundreds of nanometers. However, because we are investigating processes occurring on reduced scales, we must first thin the template layers by dry thermal oxidation followed by wet chemical etching. The final substrates consist of a thick Si handle wafer covered with 200 nm  $SiO_2$  and a single-crystal template layer tens of nanometers thick. All SOI wafers used in this study were wafer-bonded with a (001) orientation.

The wafers are chemically cleaned using standard procedures, [6] introduced into the LEEM chamber at a base pressure of  $5 \times 10^{-10}$  Torr and outgassed overnight at 650°C. The cleaning procedure covers the sample with a thin oxide layer which protects the Si template layer from decomposition at these lower temperatures. The sample is rapidly heated to 900°C and held for 50 seconds in a disilane atmosphere at  $5 \times 10^{-8}$  Torr to remove the oxide, then cooled to 650°C to be imaged by LEEM. We varied the temperature between 650°C and 980°C to change the thermal decomposition rate of the Si template layer, while monitoring the sample with LEEM in dark-field imaging mode. In this mode, contrast arises from the electron emission difference between Si and SiO<sub>2</sub> [7]. Therefore, the Si template surface appears dark and the exposed oxide surface appears bright.

Nanotubes are grown on decomposed SOI substrates by chemical vapor deposition (CVD) at a substrate temperature of 900°C with flows of  $CH_4$  at 400 sccm and  $H_2$  at 20 sccm near atmospheric pressure. Either a Fe-Pt salt solution (following the recipe in [8], diluted 10<sup>6</sup> times) is deposited or a thin Fe film is evaporated on this substrate to act as the catalyst for CNT growth. Samples were then imaged by scanning electron microscopy (SEM) or atomic force microscopy (AFM).

### DISCUSSION

Observations of decomposition and agglomeration of amorphous or polycrystalline silicon films have shown that the resulting nanocrystal distribution tends to have little or no organization [9-12]. Though such distributions may have a preferred growth direction, the initial polycrystalline structure induces an overall randomness in lateral structure. Single-crystal films, however, are typically grown epitaxially in a layer-by-layer mode (Frank-van der Merwe growth), indicating the strong attraction between the substrate and epilayer. SOI is single-crystal but not epitaxial, therefore the film can decompose without the randomness inherent in polycrystalline and amorphous films. This supplies a source of order to the distribution of agglomerated islands stemming solely from the energetics of the Si crystal. As the Si template layer thickness in SOI is reduced to the order of tens of nanometers, its thermal stability decreases. At elevated temperatures in high vacuum, the thin Si template layer dewets and beads up into Si nanocrystals, which self-order into a very distinctive pattern on the underlying amorphous oxide substrate as seen in figure 1. Although this pattern formation has been observed before [13, 14], the mechanism has not been explained.



**Figure 1**. Self-organized patterns of Si nanocrystals from agglomeration of a 9 nm thick SOI. (a) Optical image showing patterns of an agglomerated SOI wafer (1000x). (b) SEM image showing an ordered array of Si nanocrystals (bright spots) in one agglomerated pattern. (c) AFM image showing the center region of one pattern. The Si nanocrystals are faceted and 90 nm high and 300 nm wide on the average.

# Formation of nanocrystal networks by thermal dewetting of SOI

Dewetting of a thin film results from thermal diffusion of surface atoms from the surface facet to more energetically favorable ones. Some disagreement exists as to which facets exhibit the lowest free energy in silicon. Early predictions showed that the {111} facets were the lowest in energy but, more recently, the {311} facets have been found to be comparable, depending upon the temperature [15, 16]. These surface energies have been calculated from first principles,.

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but only separately by different groups [17-19]. Comparison between them is not reliable because the energies calculated for these surfaces differ by only a few meV, which is within the uncertainty of a given first-principles calculation. To make a direct comparison, we have calculated the surface energies systematically using the same method [17] and equivalent parameter settings to minimize the errors. Our calculations predict that the Si(111) and Si(311) surfaces are almost degenerate at lower temperatures; but that both the Si (311)-(3x1) and Si(311)-(1x1) reconstructions are more stable than the Si(111) surface for the higher temperatures at which the thermal dewetting takes place [20].

The selective faceting process yielding a predominant (311) facet is confirmed by AFM measurement of surface contact angles of the Si islands, as shown in figure 2. AFM line scans show a surface contact angle of  $72\pm2^{\circ}$ , indicating the large facets at the island base are indeed (311) facets. As seen in figure 1a, the islands are multifaceted. The shallower (111) and (113)



**Figure 2**. a) An AFM image of a typical silicon nanocrystal formed by SOI decomposition at  $980^{\circ}$ C and, b) its cross section. The  $72^{\circ}$  angle with the (001) substrate corresponds to a (311) facet. The facet on the right side of the island appears shallower due to the asymmetry of the AFM tip.

facets on the top of the islands are formed as the low-energy facet nearest in angle (at  $25.24^{\circ}$ ) to the (001) surface plane. Because the energy of both (311) and (113) facets are the same, the formation of each is an indication of the starting facet. The higher-angle (311) facet stabilizes first because the sidewalls formed from an initial near-vertical trench. The top of the islands is formed from atoms diffusing from the (001) surface and so the shallower (113) facet is formed.

Figure 3 shows four real-time LEEM images of a dewetting boundary front at five-second intervals with a substrate temperature of 980°C. The dark regions are the silicon template layer and nanocrystals and the light areas are the exposed SiO<sub>2</sub>. The Si layer dewets the oxide in stripes along the [130] directions, beginning at the edges of the complete Si layer, with Si atoms diffusing away to leave exposed oxide trenches. Because the channels are directed along the [130] directions, the sidewalls must have {311} facets.



**Figure 3**. Real time LEEM images taken of a SOI template during decomposition at 980°C. The time interval between successive images is 5 seconds. In this imaging mode, silicon regions appear dark while the exposed oxide is light. Labels A, B, C indicate the endpoints of trenches in the template layer. The black arrows indicate capillary instabilities in the silicon ridge which cause it to break up into individual silicon nanocrystals.

As the surface evolves from figure 3a to 3b, one can observe a trench extending from position A1 to A2 along the [130] direction; at the same time another trench forms at B1 and grows to B2 along the [310] direction. This process proceeds, in figures 3c and 3d, to form a network of trenches in <130> directions. As two adjacent trenches expand in width, the Si atoms leaving the trench area collect on the Si ridge between them. Finally, the Rayleigh instability [21] in the ridges causes them to break into individual Si nanocrystals, as indicated by the black arrows in figures 3b-d. These Si islands must line up along the <130> directions in which the ridges were originally oriented.

It has been previously observed that nucleation of agglomerated patterns in SOI occurs at thin spots in the template layer or pinhole defects [10]. Any deviation from a (100) surface lowers the kinetic barrier to the formation of a (311) facet. Therefore, by designing the locations of the nucleation sites, e.g., lithographically defining a pattern, the Si nanocrystals can be forced to align [14]. An example of this kind of forced ordering is shown in figure 4. SOI was patterned with template strips aligned in the <100> direction and then heated to dewet the silicon. Silicon islands form in a line along the <100> strip. This capability offers great potential flexibility in the design of specific device architectures for nano-electronic systems either through use as a template, as for the ordering of nanotubes discussed in the next section, or for the precise ordering and positioning of individual Si nanocrystals.

For decompositions performed at elevated temperatures (~1200°C), no ordering of Si islands along preferred directions is observed and Si (311) facets are no longer preferred. Islands are randomly distributed around the starting defect sites. We believe that the surface energy anisotropy is reduced by the increasing entropy contribution at higher temperatures.



**Figure 4**. SEM image of a line of silicon islands formed by decomposition along a <100> direction.

Furthermore, depositing several monolayers of Ge on thin SOI decreases the decomposition temperature and makes the decomposition more isotropic. We know that Ge weakens the bonds and roughens the surface at lower temperature [22, 23]. It is thus likely that the surface free energy of Ge-covered Si becomes more isotropic than that of pure Si.

### Self-assembly of nanotube networks on agglomerated SOI

While fundamentally interesting, carbon nanotubes have yet to reach their potential for practical electronic applications. This is largely due to the difficulty with which they are ordered or aligned. The most successful results in directed growth of nanotubes have made carbon nanotube networks grown from lithographically defined features [24-26] [27]. Because the main attribute required for these substrates is that they have asperities for the nanotubes to attach, the use of a self-ordering template such as the decomposed SOI eliminates a costly lithography step. As demonstrated in the previous section, however, the substrate can be patterned to align the growth of the Si nanocrystals to arrange the nanotube network into any desired pattern.

In these previous studies, catalyst is selectively positioned on the top of pillars such that nanotubes grow between pillars [24] [27]. When catalyst is distributed evenly over the sample, nanotubes grow both on top of the pillars and in the areas between the pillars [26]. In our experiment, we deposit catalyst over the entire surface of the Si nanocrystals and on the oxide between the nanocrystals. Nanotubes can in principle grow anywhere and start from any location, at the top of the islands or otherwise. Nanotubes appear, in fact, to energetically favor growth on the oxide [27]. Yet we observe a strong preference for nanotubes to form between the tops of the Si islands as seen in Figure 5.

The Si nanocrystals are about 90nm high and 100-150nm wide, with 200nm base-to-base spacing. With this geometry, the nanocrystals will likely interfere with the flow of the carbon feed gas. At our growth conditions, the mean free path of the gas molecules is on the same order as the separation of the Si islands, and the feed gas may not be able to reach the bottom areas



**Figure 5**. SEM images of carbon nanotubes deposited on an array of silicon nanocrystals formed by SOI decomposition: a) Top view shows the tubes form a network that joins the Si islands; b) Grazing angle view shows that the nanotubes connect the islands at their apexes. A transition from peak-to-peak growth to surface growth can be seen between the third and fifth arrows, counting from left to right.

between the Si nanocrystals, creating a stagnant zone. To confirm this model, we examine the effect of the Si nanocrystals on the flow pattern for our CVD growth conditions, using computational fluid dynamics (CFD) modeling software (Fluent).

In our CVD growth, the methane flow is laminar [28]. Because the growth is performed at near-atmospheric pressures, the gas transport is dominated by molecule-molecule collisions, and the flow can be described by continuum theory. As a simple first approximation, we consider only geometric effects in the simulation.

The simulation is done in two dimensions (x, z) with an array of rectangular islands on the sample surface. The islands are 90nm high, 120nm wide, with 200nm spacing on the left side of the model substrate and 400 nm spacing on the right. An illustration of this arrangement is shown in figure 6. The gas flow far away from the substrate has a uniform velocity of 3.3 mm/s, as estimated from our growth parameters [28]. Figure 6 shows that the islands introduce vertical



**Figure 6**. CFD simulation of methane flow patterns over a decomposed SOI substrate with two different nanocrystal densities. The model islands are 90 nm high and 120 nm wide. On the right half of the substrate, the islands are separated by 400 nm. On the left half, the islands are more densely packed (200 nm spacing.

oscillations in the flow pattern. An individual protrusion leaves a wake in the gas flow. The flow will regain its laminar character after a characteristic distance which is a function of the flow parameters. If the wake has not recovered before encountering the next obstacle, a stagnant region exists between the two protrusions, as seen in earlier studies of flow on rough surfaces [29]. Figure 6 shows a plot of the flow field resulting from our model.

Our simulation reveals stagnant regions (dark colored) between the islands suggesting that nanotube growth may be inhibited by the lack of methane feed gas. Although there are catalyst particles on the oxide between the islands, they are not able to capture the carbon feedstock. If the island spacing is increased, the gas flux increases near the oxide surface, as shown for the right half of the model. Chances for nanotubes to start growing from catalyst particles on the oxide should be larger as the island spacing increases. An example of this is seen in figure 2a, where nanotubes extend between the island tops for closely spaced islands yet, as soon as a large gap appears, the nanotube grows on the oxide surface.

### CONCLUSIONS

In conclusion, the template layer of ultra-thin SOI is found to dewet the oxide and agglomerate into an ordered array of Si nanocrystals at elevated temperatures in ultrahigh vacuum. We have used real-time LEEM imaging combined with first-principles calculations to identify and quantify the mechanism of nanocrystal formation and self-organization during this process. The self-organization is driven by a large surface energy anisotropy, which leads to predominant formation of nanocrystals bounded by (311) facets, as confirmed by first-principles calculations. In addition, we show that carbon nanotubes can be made to grow preferentially on the apexes of these structures by manipulating the island spacing. Lithographic patterning of SOI template layers opens the possibility for designing nanocrystal arrays as templates for self-assembling nanotube network growth.

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